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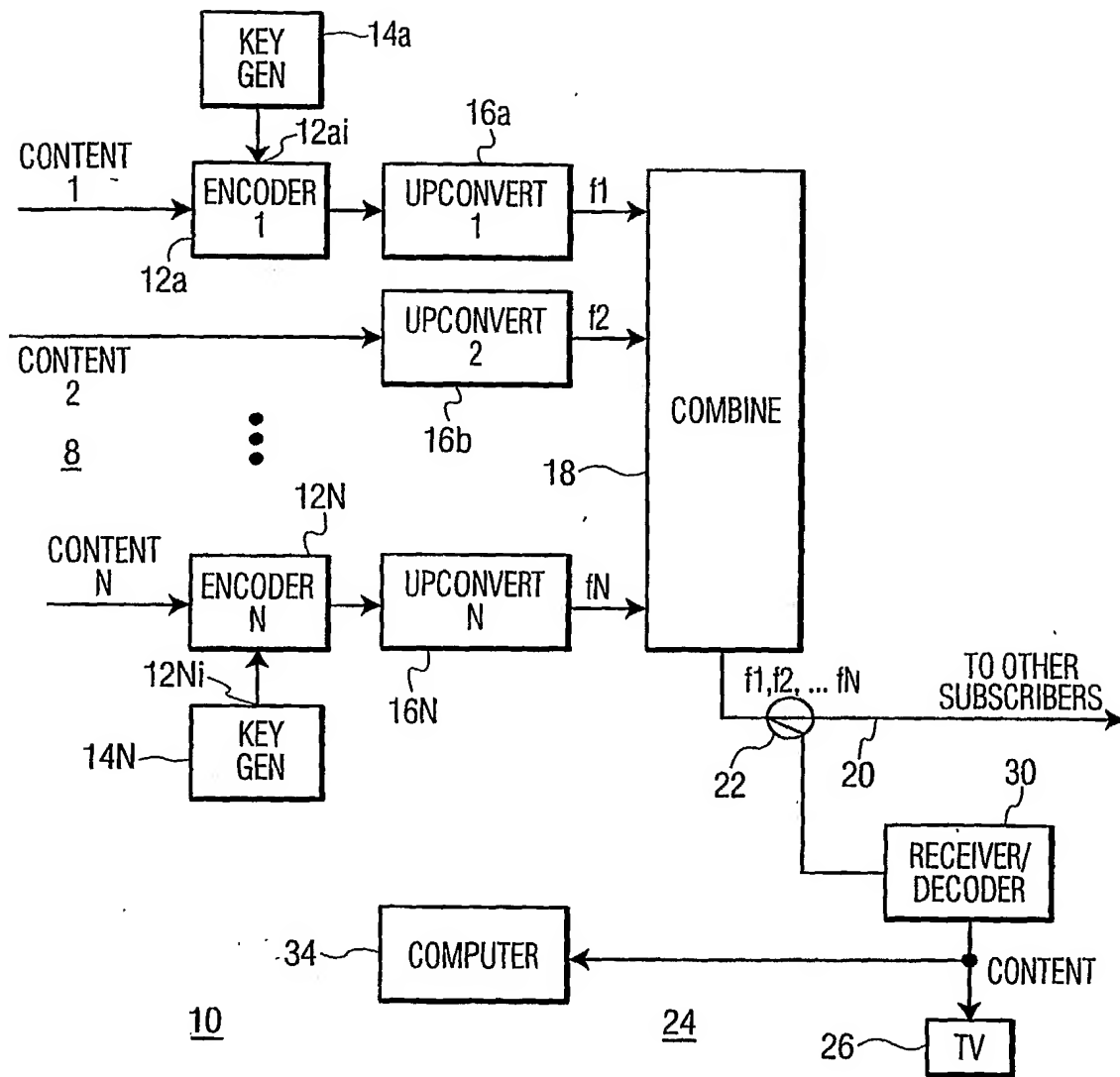
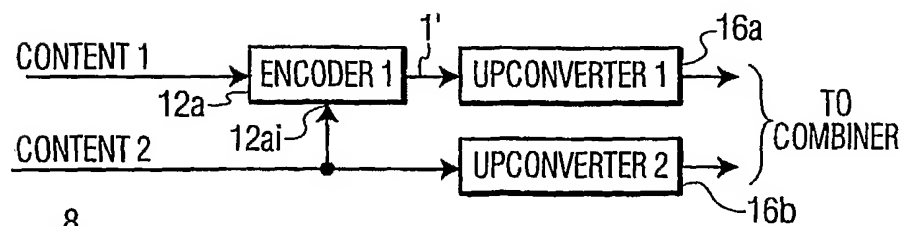
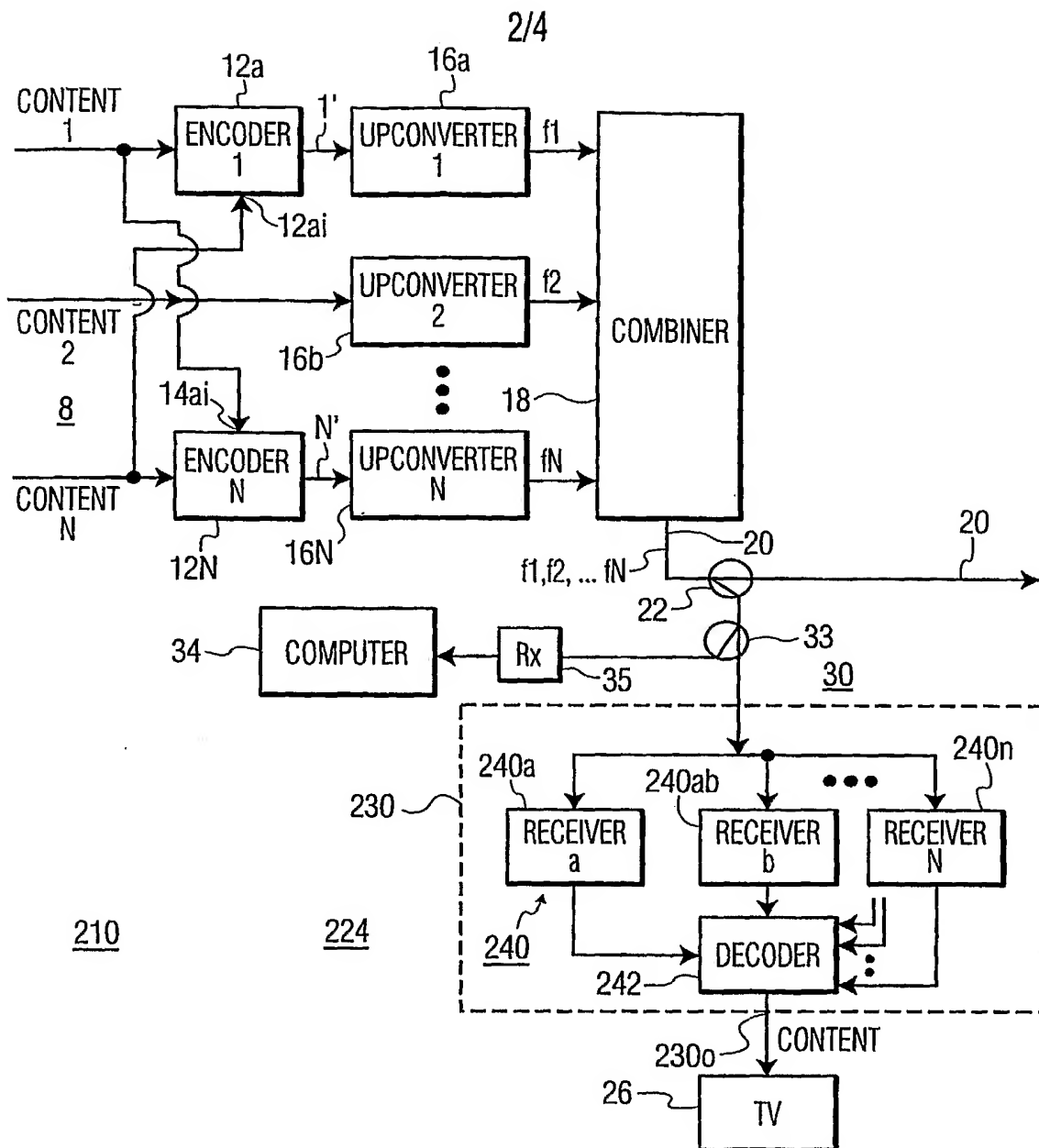


FIG. 1



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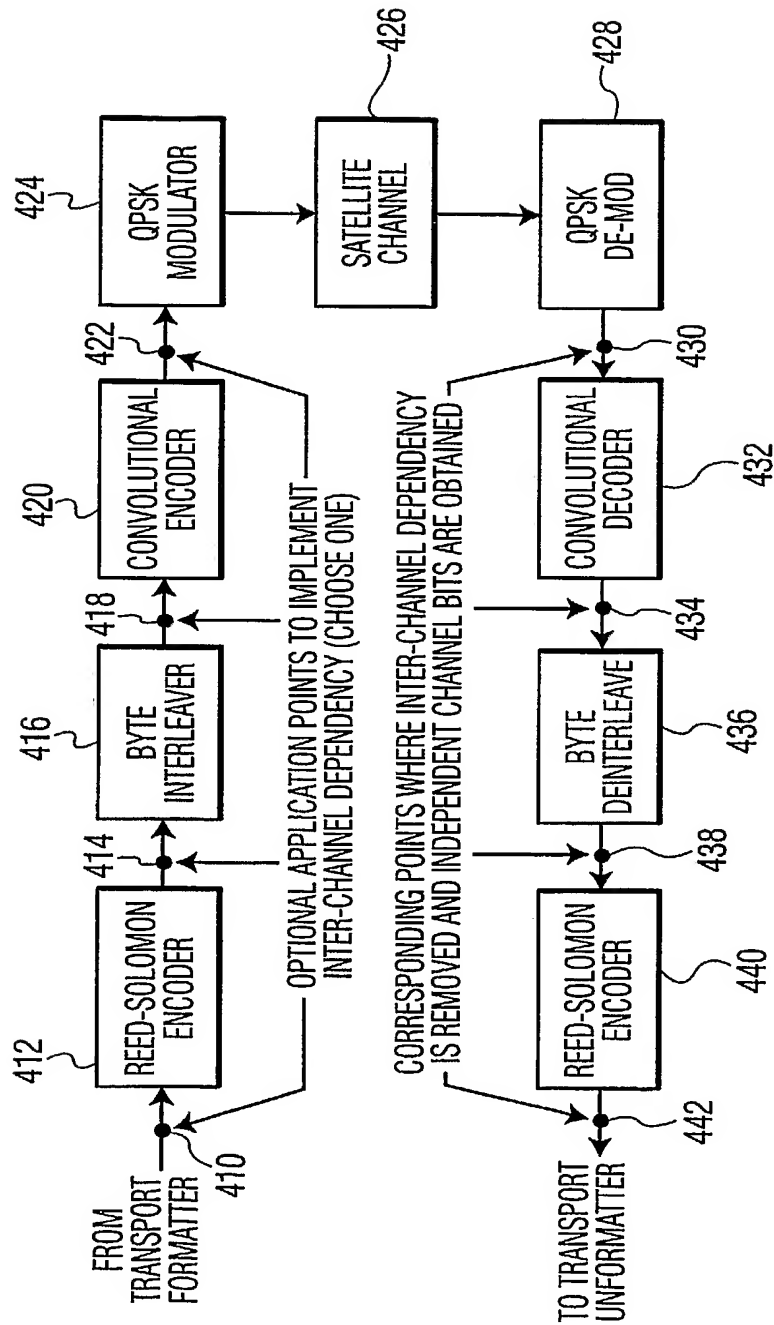
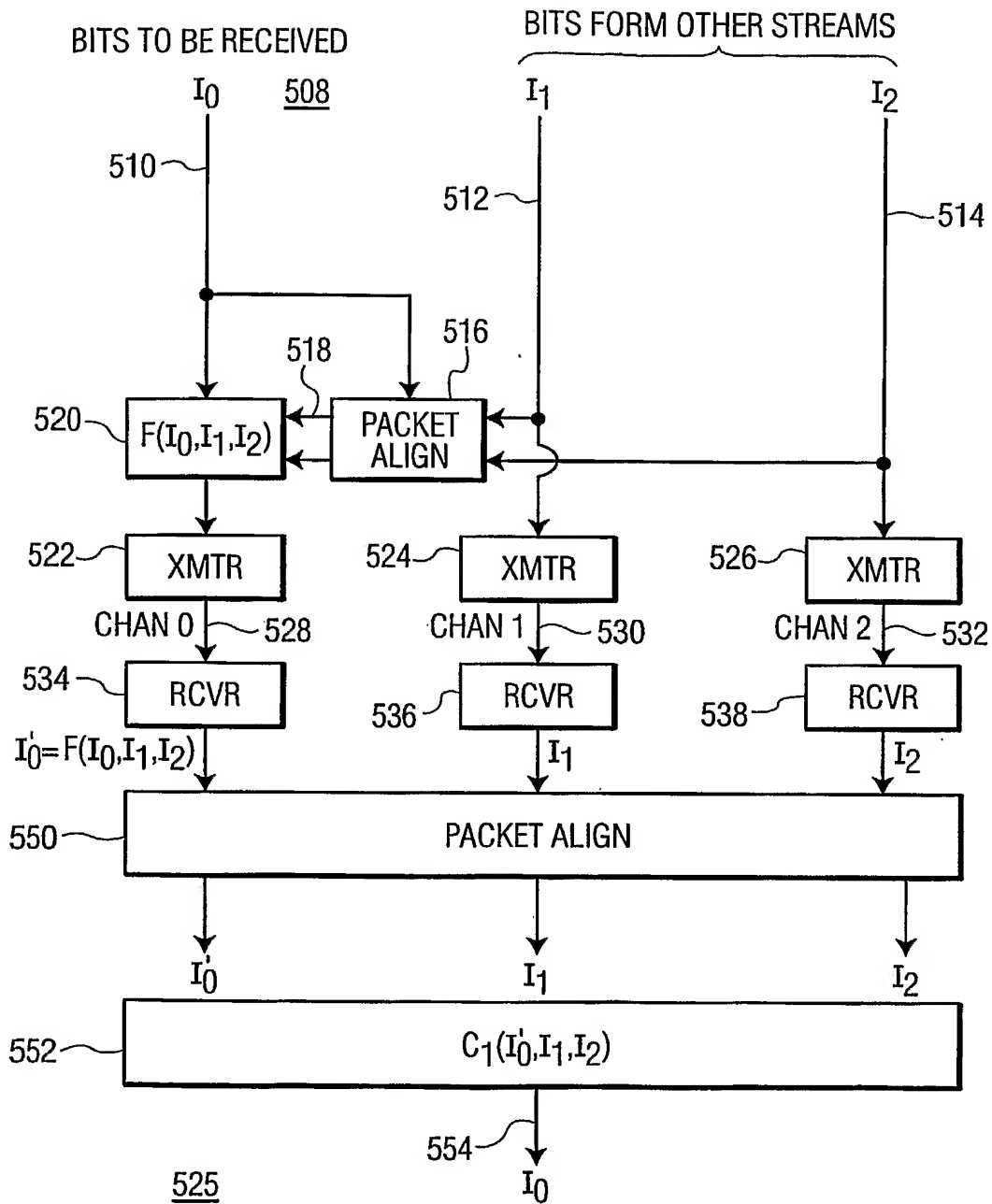


FIG. 4

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NOTE: I_0, I_1, I_2 MAY BE CONTENT
BITS OR WHOLE OR PARTIALLY
ERROR CORRECTED BITS

FIG. 5